



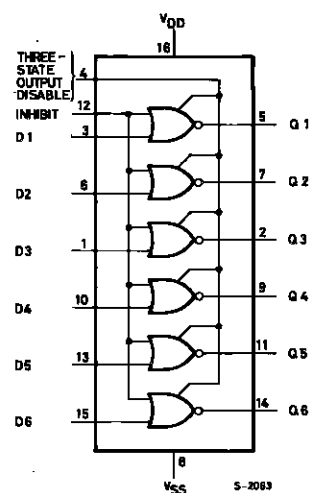
STROBED HEX INVERTER/BUFFER

- 2 TTL-LOAD OUTPUT DRIVE CAPABILITY
- 3-STATE OUTPUTS
- COMMON OUTPUT-DISABLE CONTROL
- INHIBIT CONTROL
- QUIESCENT CURRENT SPECIFIED TO 20V FOR HCC DEVICE
- 5V, 10V, AND 15V PARAMETRIC RATINGS
- INPUT CURRENT OF 100nA AT 18V AND 25°C FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDEC TENTATIVE STANDARD N°. 13A, "STANDARD SPECIFICATIONS FOR DESCRIPTION OF "B" SERIES CMOS DEVICES"

DESCRIPTION

The **CC4502** (extended temperature range) monolithic integrated circuit, available in 16-lead dual in-line plastic or ceramic package and plastic micro package. The **CC4502** consists of six inverter-buffers with 3-state outputs. A logic "1" on the OUTPUT DISABLE input produces a high-impedance state in all six outputs. This feature permits common bussing of the outputs, thus simplifying system design. A logic "1" on the INHIBIT input switches all six outputs to logic "0" if the OUTPUT DISABLE input is a logic "0". This device is capable of driving two standard TTL loads, which is equivalent to six times the JEDEC "B" series I_{OL} standard.

PIN CONNECTIONS



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DD} *	Supply Voltage :	- 0.5 to + 20	V
V _i	Input Voltage	- 0.5 to V _{DD} + 0.5	V
I _I	DC Input Current (any one input)	± 10	mA
P _{tot}	Total Power Dissipation (per package) Dissipation per Output Transistor for T _{op} = Full Package-temperature Range	200	mW
		100	mW
T _{op}	Operating Temperature :	- 55 to + 125	°C
T _{stg}	Storage Temperature	- 65 to + 150	°C

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for external periods may affect device reliability.

* All voltage values are referred to V_{SS} pin voltage.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{DD}	Supply Voltage :	3 to + 18	V
V _I	Input Voltage	0 to V _{DD}	V
T _{op}	Operating Temperature :	- 55 to + 125	°C

TRUTH TABLE

Disable	Inhibit	Dn	Qn
0	0	0	1
0	0	1	0
0	1	X	0
1	X	X	Z

X = don't care
 Z = high impedance
 Logic 1 = high
 Logic 0 = low

STATIC ELECTRICAL CHARACTERISTICS (over recommended operating conditions)

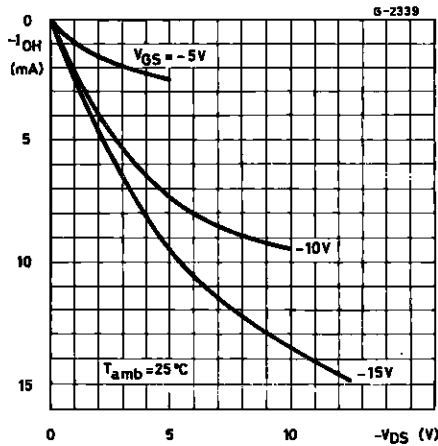
Symbol	Parameter	Test Conditions				Value						Unit	
		V _I (V)	V _O (V)	I _O (μ A)	V _{DD} (V)	T _{Low} *		25°C			T _{High} *		
						Min.	Max.	Min.	Typ.	Max.	Min.		Max.
I _L	Quiescent Current	0/ 5			5		1		0.02	1		30	μ A
		0/10			10		2		0.02	2		60	
		0/15			15		4		0.02	4		120	
		0/18			18		20		0.04	20		600	
V _{OH}	Output High Voltage	0/ 5		< 1	5	4.95		4.95			4.95		V
		0/10		< 1	10	9.95		9.95			9.95		
		0/15		< 1	15	14.95		14.95			14.95		
V _{OL}	Output Low Voltage	5/0		< 1	5		0.05			0.05		0.05	V
		10/0		< 1	10		0.05			0.05		0.05	
		15/0		< 1	15		0.05			0.05		0.05	
V _{IH}	Input High Voltage		0.5/4.5	< 1	5	3.5		3.5			3.5		V
			1/9	< 1	10	7		7			7		
			1.5/13.5	< 1	15	11		11			11		
V _{IL}	Input Low Voltage		4.5/0.5	< 1	5		1.5			1.5		1.5	V
			9/1	< 1	10		3			3		3	
			13.5/1.5	< 1	15		4			4		4	
I _{OH}	Output Drive Current	0/ 5	2.5		5	- 2		- 1.6	- 3.2		-1.15		mA
		0/ 5	4.6		5	-0.64		-0.51	- 1		-0.36		
		0/10	9.5		10	- 1.6		- 1.3	- 2.6		- 0.9		
		0/15	13.5		15	- 4.2		- 3.4	- 6.8		- 2.4		
I _{OL}	Output Sink Current	0/ 5	0.4		5	3.84		3.06	6		2.10		mA
		0/10	0.5		10	9.6		7.8	15.6		5.4		
		0/15	1.5		15	25.2		20.4	40.8		14.4		
I _{IH} , I _{IL}	Input Leakage Current	0/18		Any Input	18		\pm 0.1		\pm 10 ⁻⁵	\pm 0.1		\pm 1	μ A
I _{OH} , I _{OL}	3-state Output	0/18			18		\pm 0.4		\pm 10 ⁻⁴	\pm 0.4		\pm 12	μ A
C _I	Input Capacitance			Any Input					5	7.5			pF

CC4502

DYNAMIC ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, $C_L = 50\text{ pF}$, $R_L = 200\text{ k}\Omega$
 typical temperature coefficient for all V_{DD} values is $0.3\text{ }^{\circ}\text{C}$, all input rise and fall times = 20 ns)

Symbol	Parameter	Test Conditions		Value			Unit
			V_{DD} (V)	Min.	Typ.	Max.	
t_{PHL}	Data or Inhibit Delay Time		5		135	270	ns
			10		60	120	
			15		40	80	
t_{PLH}	Data or Inhibit Delay Time		5		190	380	ns
			10		90	180	
			15		65	30	
t_{PHZ}	Disable Delay Time (output high to high impedance)		5		60	120	ns
			10		40	80	
			15		30	60	
t_{PZH}	Disable Delay Time (high impedance to output high)		5		110	220	ns
			10		50	100	
			15		40	80	
t_{PLZ}	Disable Delay Time (output low to high impedance)		5		125	250	ns
			10		65	130	
			15		55	110	
t_{PZL}	Disable Delay Time (high impedance to output low)		5		125	250	ns
			10		55	110	
			15		40	80	
t_{TLH}	Transition Time		5		100	200	ns
			10		50	100	
			15		40	80	
t_{THL}	Transition Time		5		60	120	ns
			10		30	60	
			15		20	40	

Minimum Output High (source) Current Characteristics.



Typical Output Low (sink) Current.

